

## IMAGE CONTROL APPARATUS

### BACKGROUND OF THE INVENTION

#### (1) Field of the Invention:

5           The present invention relates to an image control apparatus, and more particularly to an image control apparatus for controlling a process of coding an image signal.

#### (2) Description of the Related Art:

10           Image coding processes such as MPEG standardized by ISO/IEC and H.262 standardized by ITU-T introduce bidirectional predictive coded images (B pictures) in addition to intraframe predictive images (I pictures) and interframe forward predictive images (P pictures).

15           Data in each of those pictures is divided into small blocks, and processed in each of the blocks. Bidirectional prediction is a process of predicting an image frame using image frames in the past and future, and uses I pictures and P pictures for prediction.

20           A resultant encoded data stream comprises an I picture at its start and some B pictures inserted between I and P pictures or between P pictures (IBBPBBPB...). A collection of pictures including an I picture at its start is called GOP (group of pictures).

25           In the conventional GOP, the number of repeated B pictures is generally 2. Such a value is suitable for average picture patterns, and other values than 2 may be

appropriate for some picture patterns. Accordingly, no optimum coding process has heretofore been carried out in the art.

For example, as an input image is closer to a still image, a suitable value for B pictures increases. Therefore, the coding efficiency increases by detecting the motion of an input image and changing the number of repeated B pictures depending on the extent of the detected motion.

When a scene change occurs, the image quality is often improved by performing the intraframe coding process. Heretofore, it has been customary to carry out the intraframe coding process with fixed timing irrespective of a scene change. The coding efficiency has been poor because the intraframe coding process has not been carried out appropriately depending on the state of input images.

#### SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide an image control apparatus which is capable of coding an image signal with increased efficiency for improved image quality.

To achieve the above object, there is provided in accordance with the present invention an image control apparatus for controlling a process of coding an image signal. The image control apparatus includes comparison processing means for carrying out at least one of a first

comparison process to compare a motion vector with a threshold, a second comparison process to compare a motion-compensated predictive error with a threshold, and a third comparison process to compare a difference value  
5 produced by subtracting the motion-compensated predictive error from an interframe difference with a threshold, and repetitive number control means for adaptively controlling the number of repeated B pictures to be inserted into a stream based on the compared result of the comparison  
10 process which is carried out by the comparison processing means.

To achieve the above object, there is also provided in accordance with the present invention an image control apparatus for controlling a process of coding an  
15 image signal. The image control apparatus includes scene change detecting means for detecting the occurrence of a scene change based on an interframe difference average representing an average of interframe differences of one picture, and picture insertion control means for inserting  
20 an I picture into a stream if a scene change is detected by the scene change detecting means.

The above and other objects, features, and advantages of the present invention will become apparent from the following description when taken in conjunction  
25 with the accompanying drawings which illustrate preferred embodiments of the present invention by way of example.

## BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram showing the principles of an image control apparatus according to the present invention;

5           FIG. 2 is a diagram showing the concept of a process of adaptively controlling the number of repeated B pictures;

FIG. 3 is a block diagram showing the principles of an image control apparatus according to the present  
10 invention;

FIG. 4 is a block diagram showing a detailed arrangement of an image control apparatus according to the present invention;

FIG. 5 is a block diagram of a coding controller  
15 in the image control apparatus shown in FIG. 4;

FIG. 6 is a block diagram of a B value controller in the coding controller shown in FIG. 5;

FIG. 7 is a block diagram of a memory in the image control apparatus shown in FIG. 4;

20           FIG. 8 is a diagram showing a matrix table managed by a matrix processor in B value controller shown in FIG. 6;

FIG. 9 is a block diagram of a scene change detector;

25           FIG. 10 is a block diagram of another scene change detector;

FIG. 11 is a block diagram of still another scene change detector;

FIG. 12 is a block diagram of yet another scene change detector;

5           FIG. 13 is a block diagram of yet still another scene change detector;

FIG. 14 is a diagram showing a state transition on a writing side;

10           FIG. 15 is a diagram showing a state transition on a writing side;

FIG. 16 is a diagram showing a state transition on a reading side;

FIG. 17 is a diagram showing a state transition on a reading side;

15           FIG. 18 is a diagram showing writing and reading sequences at the time the number of B pictures increases;

FIG. 19 is a diagram showing writing and reading sequences at the time the number of B pictures decreases;

20           FIG. 20 is a diagram showing writing and reading sequences at the time a scene changes;

FIG. 21 is a diagram showing writing and reading sequences at the time a scene changes; and

FIG. 22 is a diagram showing writing and reading sequences at the time a scene changes.

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#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

FIG. 1 shows in block form the principles of an image control apparatus 10 according to the present invention. The image control apparatus 10 serves to control a process of coding an image signal.

5           As shown in FIG. 1, the image control apparatus 10 has a comparison processing means 11 for carrying out first through third comparison processes. The first comparison process compares a motion vector (a forward predictive vector with respect to a previous frame) with a  
10 threshold. The second comparison process compares a motion-compensated predictive error with a threshold. The third comparison process compares a difference value (= an interframe difference - a motion-compensated predictive error) with a threshold. The comparison processing means  
15 11 outputs information representing the results of these comparison processes to a repetitive number control means 12.

          Based on the information representing the results of the comparison processes from the comparison processing  
20 means 11, the repetitive number control means 12 adaptively controls the number of repeated B pictures in a GOP. In FIG. 1, the number of repeated B pictures is 3.

          A process of adaptively controlling the number of repeated B pictures will be described below. FIG. 2 shows  
25 the concept of the process of adaptively controlling the number of repeated B pictures. A direction F for controlling the number of repeated B pictures is

determined by the sum of a direction  $F(v)$  for controlling the number of repeated B pictures which is determined by a relation diagram Z1 and a direction  $F(e)$  for controlling the number of repeated B pictures which is determined by an inequality Z2.

The relation diagram Z1 has a horizontal axis representing a motion vector and a vertical axis a motion-compensated predictive error. As the direction  $F(v)$  which is determined by the motion vector and the motion-compensated predictive error is pointed obliquely upward to the left in the relation diagram Z1, the number of repeated B pictures increases, and as the direction  $F(v)$  is pointed obliquely upward to the right, the number of repeated B pictures decreases.

When the value of the motion vector is smaller, since an image to be coded is closer to a still image, the direction  $F(v)$  is controlled to increase the number of repeated B pictures.

When the motion-compensated predictive error is smaller, since an image to be coded is closer to a still image or the motion vector is detected properly, the direction  $F(v)$  is controlled to increase the number of repeated B pictures.

As the number of repeated B pictures increases, the time difference between a reference frame and a frame to be coded increases. Therefore, a tendency arises to detect the motion vector improperly and increase the

motion-compensated predictive error. Specifically, when the number of repeated B pictures increases, the proportion of B pictures in the GOP increases and the coding efficiency increases. However, because the effect of motion compensation tends to be reduced, the direction  $F(v)$  should be controlled at a well balanced value in view of a trade-off between positive aspects resulting from the increased number of repeated B pictures and negative aspects resulting from the improper detection of the motion vector.

The inequality Z2 represents the comparison between the difference value produced by subtracting the motion-compensated predictive error from the interframe difference and the threshold. If the difference value is greater than the threshold, then the direction  $F(e)$  increases the number of repeated B pictures. If the difference value is smaller than the threshold, then the direction  $F(e)$  reduces the number of repeated B pictures.

If the value of the motion-compensated predictive error is not smaller than the value of the interframe difference, then since the motion detection does not function well, the direction  $F(e)$  is controlled to reduce the number of repeated B pictures. Otherwise, the direction  $F(e)$  is controlled to increase the number of repeated B pictures.

An image control apparatus according to the present invention for inserting an I picture when a scene



change occurs will be described below. FIG. 3 shows in block diagram of such an image control apparatus 20. The image control apparatus 20 serves to control a process of coding an image signal.

5           As shown in FIG. 3, the image control apparatus 20 has a scene change detecting means 21 for detecting the occurrence of a scene change based on an interframe difference average of interframe differences of the same picture.

10           The interframe difference average represents an average of differences between blocks or pixels in a frame ( $t - 1$ ) and a frame  $t$  which are of the same picture, e.g., P picture.

15           The image control apparatus 20 also has a picture insertion control means 22 for inserting an I picture into a stream, i.e., performing an intraframe coding process, if a scene change is detected by the scene change detecting means 21.

20           The detection of a scene change will be described below. If one of the following situations occurs, then it is regarded as the occurrence of a scene change.

(1) The interframe difference average is greater than a threshold. This situation is considered to be a scene change in the entire view.

25           (2) The difference between a block average of pixel data in each of blocks converted from a picture and the interframe difference average is determined, and there

are many blocks where the determined difference is large. This situation is considered to be a scene change in a partial view, e.g., the background remains unchanged and a person suddenly appears in the scene.

5           (3) The combination of the situations (1) and (2). Specifically, the interframe difference average is greater than a threshold, and also the difference between a block average of pixel data in each of blocks converted from a picture and the interframe difference average is  
10 determined, and there are many blocks where the determined difference is large.

          (4) A change in the interframe difference average is greater than a given value and represents an abrupt change.

15          (5) The interframe difference average is lower than a threshold, and also a change in the interframe difference average is greater than a given value and represents an abrupt change.

          An image control apparatus which has the functions  
20 of both the image control apparatus 10, 20 will be described below. FIG. 4 shows in block form such an image control apparatus 30.

          The image control apparatus 30 is a specific apparatus which performs the function to adaptively  
25 control the number of repeated B pictures and the function to insert an I picture upon the occurrence of a scene change.

As shown in FIG. 4, the image control apparatus 30 has a fixed delay unit 31 for delaying input image data for a time required to detect a scene change for time adjustment, and a motion vector detector 32 for detecting  
5 a motion vector between a frame  $(t - 1)$  and a frame  $t$  and transmitting the value of the detected motion vector to a coding controller 4. While motion compensation (also referred to as MC) is also carried out by an encoder 37, the motion vector detector 32 detects a motion vector in  
10 advance for determining the number of repeated B pictures.

The image control apparatus 30 also has an MC predictive error detector 33 which shifts a stored previous frame according to the motion vector, detects an MC predictive error which represents a shift from the  
15 present frame, and transmits the value of the detected MC predictive error to the coding controller 4.

The image control apparatus 30 also has an interframe difference detector 34 for detecting an interframe difference between a frame  $(t - 1)$  and a frame  
20  $t$ , and a fixed delay unit 35 for delaying a received interframe difference for adjusting the time of a process carried out by a scene change detector 100 and transmitting the value of the delayed interframe difference to the coding controller 4.

25 The scene change detector 100 detects a scene change based on the interframe difference, and transmits a

1-bit signal representing whether a scene change has occurred or not to the coding controller 4.

Based on a frame CLK (clock), the coding controller 4 processes the supplied data, generates a memory control signal to control a memory 36, and applies the memory control signal to the memory 36. The coding controller 4 also transmits a predictive code signal indicative of an I, P, or B picture to the encoder 37.

Based on the memory control signal from the coding controller 4, the memory 36 writes or reads image data transmitted from the fixed delay unit 31.

The encoder 37 encodes an image signal transmitted from the memory 36 based on the predictive mode signal from the coding controller 4. For example, if the predictive mode signal indicates an I picture, then the encoder 37 performs an intraframe predictive coding process on an image signal transmitted from the memory 36.

FIG. 5 shows in block form the coding controller 4. The controller 4 comprises a B value controller 40 and a state transition block 400.

The B value controller 40 determines an increase (Inc), a hold (H), or a decrease (Dec) of the number of repeated B pictures based on a motion vector, an MC predictive error, and an interframe difference.

The state transition block 400 controls transitions of I, P, B pictures when a scene change is detected and when the

B value controller 40 determines that the number of repeated B pictures needs to be increase or decrease.

FIG. 6 shows in block form the B value controller 40. The B value controller 40 has a comparator 41 for  
5 comparing a motion vector with a threshold and accumulators 42a, 42b for accumulating MC predictive errors and interframe differences in each block in response to a reset signal in each block, which is a basic unit in image coding applications.

10 The B value controller 40 has a comparator 43 for comparing an accumulated MC predictive error with a threshold, a subtractor 44 for subtracting an accumulated MC predictive error from an accumulated interframe difference, and a comparator 45 for comparing the  
15 difference with a threshold.

The B value controller 40 also has a matrix processor 46 for receiving compared results from the comparators 41, 43, 45 and determining and outputting Inc/H/Dec in each block based on a decision reference  
20 established therein.

Counters 47a - 47c counts blocks to be increased, held, or decreased per frame (one view) based on a block CLK. A weighting unit 48 weights counts outputted from the counters 47a - 47c, and outputs respective signals W1  
25 - W3. A selector 49 selects one of the signals W1 - W3 which represents the greatest number of blocks, and uses the selected signal as final determined results Inc/H/Dec.

FIG. 7 shows the memory 36 in detail. As shown in FIG. 7, the memory 36 comprises three FIFO memories 36a - 36c for storing I, B, P pictures, an OR gate 36d, and a frame memory 36e for increasing the number of P pictures.

5           Based on a memory control signal from the coding controller 4, the memory 36 separates I/ P/ B pictures and stores them in the respective FIFO memories 36a - 36c. When output data are to be transmitted from the memory 36 to the encoder 37, image frames need to be rearranged  
10 according to I/B/P pictures. Such rearrangement of image frames is performed by changing selected ones of the FIFO memories 36a - 36c from which to read the I, B, P pictures. For increasing the number of B pictures, one repetition of P pictures occurs. For reducing the number of B pictures,  
15 one B picture is discarded.

Signals WE-I, B, P, RE-I, B, P, a discard flag, a and a signal RE-P (increase) are included in the memory control signal.

FIG. 8 shows a matrix table T managed by the  
20 matrix processor 46 shown in FIG. 6. The matrix processor 46 uses the matrix table T to relate the determined results Inc/H/Dec as shown based on 3-bit information representing the results of comparison of the motion vector, the MC predictive error, and the interframe  
25 difference with the thresholds. In the matrix table T, "1" represents a condition of being greater than the threshold,

and "0" represents a condition of being smaller than the threshold.

The scene change detector 100 will be described below. FIGS. 9 through 13 show different scene change  
5 detectors which correspond respectively to the situations (1) - (5), described above, relative to the occurrence of a scene change.

FIG. 9 shows in block form a scene change detector 100-1. The scene change detector 100-1 has an accumulator  
10 111 for accumulating an interframe difference of each frame in response to a frame reset signal, and an average processor 112 for averaging accumulated interframe differences to generate an interframe difference average according to a frame CLK.

15 The scene change detector 100-1 also has a comparator 113 for comparing the interframe difference average with a threshold. If the interframe difference average is greater than the threshold, then the comparator 113 detects the occurrence of a scene change. At this time,  
20 the comparator 113 produces an output signal OUT of "1", for example.

FIG. 10 shows in block form another scene change detector 100-2. The scene change detector 100-2 has an  
25 accumulator 121 for accumulating an interframe difference of each frame in response to a frame reset signal, and an average processor 122 for averaging accumulated interframe

differences to generate an interframe difference average according to a frame CLK.

The scene change detector 100-2 also has an accumulator 124 for accumulating an interframe difference of each block and generating a block average in response to a block reset signal.

In order to determine whether the difference between the interframe difference average and the block average is large or not, an adder 123 adds an offset to the interframe difference average. A comparator 125 compares the interframe difference average to which the offset is added with the block average, and outputs a 1-bit signal indicative of whether the difference therebetween is large or not, in each block.

In response to a frame reset signal, a counter 126 counts an output signal from the comparator 125 according to a block CLK, and outputs a count in each frame.

A reading unit 127 reads the count from the counter 126 in response to a frame CLK, and a comparator 128 compares the read count with a threshold. If the count is greater than the threshold, then the comparator 128 detects the occurrence of a scene change. At this time, the comparator 128 produces an output signal OUT of "1", for example.

FIG. 11 shows in block form still another scene change detector 100-3. Those parts of the scene change detector 100-3 which are identical to those of the scene



change detector 100-2 shown in FIG. 10 are denoted by identical reference characters, and will not be described in detail below.

The scene change detector 100-3 has a comparator 131 for comparing the interframe differential average from the average processor 122 with a threshold. If the interframe differential average is greater than the threshold, then the comparator 131 produces an output signal OUT2 of "1". The scene change detector 100-3 also has an AND gate 132 for ANDing the output signal OUT1 from the comparator 128 and the output signal OUT2 from the comparator 131. The AND gate 132 produces an output signal of "1" when a scene change occurs.

FIG. 12 shows in block form yet another scene change detector 100-4. The scene change detector 100-4 has an accumulator 141 for accumulating an interframe difference of each frame in response to a frame reset signal, and an average processor 142 for averaging accumulated interframe differences to generate an interframe difference average according to a frame CLK.

A reading unit 143 reads the interframe difference average according to a frame CLK, and a reading unit 144 reads the interframe difference average from the reading unit 143 according to a frame CLK.

A subtractor 145 determines the difference between the interframe difference average (on the previous frame side) outputted from the reading unit 143 and the

interframe difference average (on the present frame side)  
outputted from the reading unit 144.

A comparator 146 compares the difference from the  
subtractor 145 with a threshold. If the difference is  
5 greater than the threshold, then the comparator 146  
detects the occurrence of a scene change. At this time,  
the comparator 146 produces an output signal OUT of "1",  
for example.

FIG. 13 shows in block form yet still another  
10 scene change detector 100-5. Those parts of the scene  
change detector 100-5 which are identical to those of the  
scene change detector 100-4 shown in FIG. 12 are denoted  
by identical reference characters, and will not be  
described in detail below.

15 The scene change detector 100-5 has a reading unit  
151 which reads the difference outputted from the  
subtractor 145, and a comparator 152 which compares the  
difference IN outputted from the reading unit 151 with a  
threshold. If the difference IN is smaller than the  
20 threshold ( $IN < \text{threshold}$ ), then the comparator 152  
regards the scene as stable and produces an output signal  
OUT1 of "1".

Four flip-flops FF 154 hold respective signals of  
"1" in response to a frame CLK, and output the signals of  
25 "1" to an AND gate 155. If all the output signals from the  
flip-flops FF 154 are "1", then frames of the same picture  
pattern are in succession. A comparator 153 compares the

difference IN outputted from the reading unit 151 with a threshold. If the difference IN is greater than the threshold ( $IN > \text{threshold}$ ), then the comparator 153 produces an output signal OUT2 of "1" to the AND gate 155.

5           If all the input signals applied to the AND gate 155 are "1", i.e., if the scene changes from a stable state to an abruptly changing state, then the AND gate 155 produces an output signal OUT2 of "1" representing the occurrence of a scene change.

10           A state transition process carried out by the state transition block 400 will be described below. FIGS. 14 and 15 are diagrams showing state transitions on a writing side, and FIGS. 16 and 17 are diagrams showing state transitions on a reading side. FIG. 18 is a diagram  
15           showing writing and reading sequences at the time the number of B pictures increases, and FIG. 19 is a diagram showing writing and reading sequences at the time the number of B pictures decreases.

20           First, operation on the writing side will be described below. Common to subsequent state transition operations are that a state transition is performed in each frame and that the number of repeated B pictures is increased and decreased and a state variable is set due to a scene change out of synchronism with each other.

25           First, in an INIT state, the repetitive period of B pictures is set to M0, the number of I or P pictures in the GOP is set to N0, and variables are reset. The state

immediately after initialization is a B state. M0 and N0 are set to temporal variables N, M, respectively. For a closed GOP, a BB flag is set in order to inhibit forward prediction (this corresponds to a writing sequence B1 in FIG. 18).

Since  $N = 3$ ,  $M = 2$ ,  $BB = \text{set}$  in this state, the condition of " $M \neq 1$  and  $INC1 = DEC = 0$ " is satisfied. A next state to transit to is the B state, as can be seen from FIGS. 14 and 15 (this corresponds to a writing sequence B2 in FIG. 18).

As shown in FIGS. 14 and 15, since the value of M is decremented by "1" when returning to the B state,  $N = 3$ ,  $M = 1$ ,  $BB = \text{set}$  in this state (B2). Therefore, the condition of " $M = 1$  and  $N = 3$ " is satisfied, and a next state to transit to is an I state, as can be seen from FIGS. 14 and 15.

As shown in FIGS. 14 and 15, when entering the I state, BB is reset thereby inhibiting forward prediction of a B picture at the GOP boundary and realizing a closed GOP. The I state unconditionally transits to the B state (this corresponds to a writing sequence B3 in FIG. 18).

When entering the B state, the value of N is decremented by 1, the value of M is returned to the initial value, and DEC2, INC2 are reset. Since DEC2, INC2 are not originally set,  $N = M = 2$ , and all other flags are reset. Therefore, the condition of " $M \neq 1$  and  $INC1 = DEC = 0$ " is satisfied, and a next state to transit to is the B

state again, as can be seen from FIGS. 14 and 15 (this corresponds to a writing sequence B4 in FIG. 18).

As shown in FIGS. 14 and 15, since the value of M is decremented by "1" when returning to the B state, N = 2, M = 1 in this state. Therefore, the condition of "M = 1 and N  $\neq$  3" are satisfied, and a next state to transit to is a P state (this corresponds to a writing sequence P1 in FIG. 18).

In this state, inasmuch as N = 2, M = 0, CHG  $\neq$  1, a next state to transit to is the B state. When returning to the B state, the value of N is decremented by 1, the value of B is set again, and DEC2, INC2 are reset.

If the number of repeated B pictures is neither increased nor decreased and a scene change does not occur, the state transits to B, B, P, B, B, P, B, B, P, B, ... If the value of N is 0 when entering the P state, then the next state is the B state as is the case with the other situations, but the value of N is returned to the initial value unlike the other situations. In this manner, transitions (BBI BBP BBP BBP B...BP BBP) (BBI BBP ...) at the GOP period can be performed.

Operation on the reading side will be described below. The reading side starts a fixed delay later than the writing side. A first state is an I state (see FIGS. 16 and 17), and M0, N0 are put into respective temporal variables N, M. A next state to transit to is unconditionally the B state, as shown in FIGS. 16 and 17

(this corresponds to a reading sequence B1 in FIG. 18). Since the value of M is decremented by "1" when returning to the B state,  $M = 1$ ,  $N = 3$  in this state. Therefore, the condition of " $M \neq 0$  and DecSet  $\neq 1$ " is satisfied, and a  
5 next state to transit to is also the B state (this corresponds to a reading sequence B2 in FIG. 18).

Since the value of M is decremented by "1" when returning to the B state,  $M = 0$ ,  $N = 3$  in this state. Therefore, the condition of " $M = 0$  and  $N \neq 0$ " is satisfied,  
10 and a next state to transit to is the P state (this corresponds to a reading sequence P1 in FIG. 18).

When transiting to the P state, the value of M is set again and the value of N is decremented by 1. In this state, therefore,  $M = 2$ ,  $N = 2$ . If the number of repeated  
15 B pictures is not increased and a scene change does not occur, then since the condition of " $Incset = 0$ " is satisfied, a next state to transit to is the B state. Subsequently, the state transits to B, B, P, B, B, P, B, B, P, B, ....

20 If the value of N is 0 when entering the B state and  $M = 0$  while the B state is being looped, the B state transits to the I state, returning to the initial state (the leading end of the GOP), as shown in FIGS. 16 and 17. In this manner, transitions (IBB PBB PBB PBB PB...PBB)  
25 (IBB PBB ...) at the GOP period can be performed.

Operation at the time the number of repeated B pictures is increased will be described below. An

increase in the number of repeated B pictures is monitored in the B state on the writing side (see FIGS. 14 and 15). If a process of requesting a previous increase has been finished ( $INC2 \neq 1$ ), the value of M does not exceed the maximum value, and an increase is determined while in the B state on the writing side, an increase request flag  $INC1/2$  is set to asynchronism, and the values of M,  $M0$  are incremented by 1. The B state is looped until the value of M becomes 1.

10           If an increase is determined in a writing sequence B5 in FIG. 18, then as shown in FIG. 18,  $INC1, 2$  are set at the terminal end of the writing sequence B5, the value of  $M0$  is incremented to 3 by 1, and the value of M is incremented from 2 to 3 by 1. Since the condition of  
15   " $INC1 = 1$ " is satisfied, a next state of the writing sequence B5 to transit to is the B state again, as can be seen from FIGS. 14 and 15.

When transiting to a writing sequence B6 shown in FIG. 18, the value of M is decremented by 1 and  $INC1$  is  
20 reset, as shown in FIGS. 14 and 15. In the writing sequence B6, therefore,  $M = 2$ ,  $INC1 = 0$ ,  $INC2 = 1$ . According to the transition diagrams shown in FIGS. 14 and 15, the B state is looped until the value of M becomes 1.

Because the condition of " $M = 1$  and  $N \neq 3$ " is  
25 satisfied in a writing sequence B7 shown in FIG. 18, a next state to transit to is the P state. Since  $INC2 = 1$  in a writing sequence P2 shown in FIG. 18, a flag IncSet for

increasing the number of repeated B pictures on the reading side is set to 1.

A next state of the writing side to transit to is the B state as  $CHG \neq 1$ . When transiting to a writing sequence B8 shown in FIG. 18, N is decremented, M is set again, and INC2 is reset. Subsequently, until M becomes 1, the B state is looped, and after the B state transits to the P state, the P state returns to the B state. If the condition of "N = 0 and M = 0" is satisfied (e.g., a writing sequence P3 shown in FIG. 18) when transiting to the P state, then N is initialized to initialize the GOP when transiting to the B state.

INC2 is used as a condition to determine an increase the number of repeated B pictures in order to limit the unit by which to increase the number of repeated B pictures between I or P pictures to 1. As shown in FIG. 18, usually, there is a time difference of about one picture between I and P pictures that are written and between I and P pictures that are read. Since the number of repeated B pictures between I and P pictures or P and P pictures is at most 1, if the number of repeated B pictures is increased on the writing side, I, P pictures are written and read substantially in phase with each other (they are read slightly later than they are written).

Therefore, as shown in FIG. 18, if IncSet is set on the writing side, then IncSet appears in phase with the P state on the reading side. On the reading side, if



IncSet is set at the time of transiting to the P state (a reading sequence P2 shown in FIG. 18, then as shown in the transition diagrams of FIGS. 16 and 17, the P state transits to a PInc state. When transiting to the PInc state, the value of M is incremented by 1 and IncSet is reset. A flag indicative of an increase in the number of repeated P pictures is outputted while in the PInc state.

The flag indicative of an increase in the number of repeated P pictures is connected to REN-P (increase) in the block diagram of the memory 36, and data of a P2 state is outputted again by outputting data on an FM side. Transitions after the PInc state are the same as with the ordinary P state, except that the number of repeated B pictures increases as the value of M is incremented by 1.

Operation at the time the number of repeated B pictures is decreased will be described below. A decrease in the number of repeated B pictures is monitored in the B state on the writing side (see FIGS. 14 and 15). If a process of requesting a previous decrease has been finished, the value of M exceeds 1, and a decrease is determined while in the B state on the writing side, a decrease request flag DEC1/2 is set to asynchronism, and the values of M, M0 are decremented by 1. The B state is looped until the value of M becomes 1.

If a decrease is determined in a writing sequence B5 in FIG. 19, then as shown in FIG. 19, DEC1, 2 are set at the terminal end of the writing sequence B5, the value

of M0 is decremented to 1 by 1, and the value of M is decremented from 2 to 1 by 1. In FIGS. 14 and 15, since the condition of "M = 1 and N  $\neq$  3" is satisfied, a next state of the writing sequence B5 to transit to is the P state.

When transiting to a writing sequence P2 shown in FIG. 19, the value of M is decremented by 1, DEC1 is reset, and DecSet is set, as shown in FIG. 14. In the writing sequence P2 shown in FIG. 19, therefore, M = 0, DEC1 = 0, DEC2 = 1. According the transition diagrams shown in FIGS. 14 and 15, the B state is looped until the value of M becomes 1. In this case, since the value of M is 1 from the outset, the state transits alternately between the B and P states. If the condition of "N = 0 and M = 0" is satisfied when transiting to the P state (e.g., a writing sequence P3 shown in FIG. 19), the GOP is initialized by initializing N when transiting to the B state.

DEC2 is used as a condition to determine a decrease in the number of repeated B pictures in order to limit the unit by which to decrease the number of repeated B pictures between I or P pictures to 1. Since no mode of reducing the number of repeated B pictures is carried out unless M is 2 or more, as can be seen from FIG. 19, if DecSet is 1, the reading sequence is in the B state. Consequently, DecSet is monitored in the B state in the transition diagrams of FIGS. 16 and 17 (READ).

In the example of FIG. 19, DecSet is set while in the reading sequence B3. In this case, since  $N = 2$ ,  $M = 1$ ,  $\text{DecSet} = 1$ , the condition of " $M \leq 1$  and  $\text{DecSet} = 1$ ,  $N \neq 0$ " is satisfied, and a next state to transit to in FIGS. 5 16 and 17 is a P Dec state. When transiting to the P Dec state, the value of  $N$  and the value of  $M0$  are decremented by 1,  $M$  is initialized, and DecSet is reset. While in this state, since it is the P state, the data of a P frame is read. At the same time, the data of a B frame is also read 10 by outputting a discard flag. However, this data (B4) is not used, but discarded.

A next state to transit to from the P Dec state is unconditionally the B state, as can be seen from FIGS. 16 and 17. Subsequently, the state transits alternately 15 between the B and P states. When the condition of " $N = 0$  and  $M = 0$ " is satisfied (e.g., a reading sequence B6 shown in FIG. 19), the state transits to the I state with next timing, initializing each GOP.

Operation upon a scene change will be described 20 below. A scene change is monitored in the B state and the P state on the writing side. Since the detection of a scene change precedes the other processes by 2 frames, the scene change is processed 2 frames prior to a scene changing view.

25 For example, if a scene change occurs at  $P2 \rightarrow I2$  shown in FIG. 20, then the scene change is processed at B5, B6 - P, and  $P2 \rightarrow I2$ . On the writing side, the value of  $M$

is held as MC, and the state is transited via the P state to the I state, forcibly setting the frame of the scene change to an I picture, and fixing a picture at the terminal end of the GOP on the writing side to P.

5           On the reading side, subsequent transitions are controlled according to a ChgSet flag and the value of MC in the P state. If ChgSet = 1 and MC = 0, then the state is transited to the I state, and thereafter to the P state, after which ordinary transitions are performed. If ChgSet  
10   = 1 and MC  $\neq$  0, then a B CHG state is transited  $M0 + 1 - MC$  times, after which the state is transited to the I and P states, and thereafter ordinary transitions are performed.

          Operation will be described below with reference  
15   to FIGS. 20 through 22. If a picture at a writing sequence  $P2 \rightarrow I2$  shown in FIG. 20 is a scene change picture, then a scene change is detected at B5. In the B state shown in FIGS. 14 and 15, MC and CHG1 are set to MC = 1 and CHG1 = 1. Since the condition of "(M = 1 and N  $\neq$  3" or CHG1 = 1")  
20   is satisfied in the B state shown in FIGS. 14 and 15, a next state to transit to is the P state (B6 - P in FIG. 20). When entering this state, the value of M is decremented by 1, resulting in M = 1.

          Since the condition of "CHG1 = 1" is satisfied in  
25   the P state in FIGS. 14 and 15, a next state to transit to is the I state ( $P2 \rightarrow I2$  in FIG. 20). When entering this

state, CHG1, M, N are initialized, and a scene change detecting flag ChgSet on the reading side is set to 1.

A next state to transit to is unconditionally the B state, after which ordinary transitions are performed.

5 On reading side, since operation starts with the number of repeated B pictures being 2, the state transits from I1 → B1 → B2 → P1 → B3 → B4 → P(B6 - P). In the P state shown in FIGS. 16 and 17, since the condition of "ChgSet = 1, MC ≠ 0" is satisfied, a next state to transit to is a B  
10 CHG state (a reading sequence B5 shown in FIG. 21).

Since CNT = 2 - 2 - 1 = 1, the condition of "CNT ≤ 1" is satisfied, and a next state to transit to is an I CHG state (a reading sequence I2 shown in FIG. 21). At this time, N is initialized, and the state is  
15 unconditionally transited to the P state (a reading sequence P3 shown in FIG. 21) with next timing. When transiting to the P state, N is decremented, M is initialized, and ChgSet is reset, after which ordinary operation is performed.

20 Therefore, as shown in FIG. 20, it is possible to set a scene changing view to an I picture and inhibit a reference to a predicted value across and over the scene changing view.

If a picture at a writing sequence B7 → I2 shown  
25 in FIG. 21 is a scene change picture, then a scene change is detected at B6. Then, MC and CHG1 are set to MC = 1 and CHG1 = 1 in the B state shown in FIGS. 14 and 15.

Since the condition of " $(M = 1 \text{ and } N \neq 3 \text{ or } CHG1 = 1)$ " is satisfied in the B state shown in FIGS. 14 and 15, a next state to transit to is the P state (P2Chg in FIG. 21). When entering this state, the value of M is  
5 decremented by 1, resulting in  $M = 0$ .

Since the condition of " $CHG1 = 1$ " is satisfied in the P state in FIGS. 14 and 15, a next state to transit to is the I state ( $B7 \rightarrow I2$  in FIG. 21). When entering this state, CHG1, M, N are initialized, and the scene change  
10 detecting flag ChgSet on the reading side is set to 1.

A next state to transit to is unconditionally the B state, after which ordinary transitions are performed. On reading side, since operation starts with the number of repeated B pictures being 2, the state transits from  $I1 \rightarrow$   
15  $B1 \rightarrow B2 \rightarrow P1 \rightarrow B3 \rightarrow B4 \rightarrow P2$ . In this state, since the condition of " $ChgSet = 1, MC \neq 0$ " is satisfied, a next state to transit to is the B CHG state (the reading sequence B5 shown in FIG. 21). Since  $CNT = 2 - 1 - 1 = 2$ , the condition of " $CNT > 1$ " is satisfied, after the state  
20 transits to the B CHG state once again (a reading sequence B6 shown in FIG. 21), and thereafter to the I CHG state (the reading sequence I2 shown in FIG. 21).

At this time, N is initialized, and the state is unconditionally transited to the P state (the reading  
25 sequence P3 shown in FIG. 21) with next timing. When transiting to the P state, N is decremented, M is initialized, and ChgSet is reset, after which ordinary

operation is performed. Therefore, as shown in FIG. 21, it is possible to set a scene changing view to an I picture and inhibit a reference to a predicted value across and over the scene changing view.

5           If a picture at a writing sequence  $B8 \rightarrow I2$  shown in FIG. 22 is a scene change picture, then a scene change is detected at P2. Then, MC and CHG2 are set to  $MC = 0$  and  $CHG2 = 1$  in the P state shown in FIGS. 14 and 15.

10           Since the condition of " $CHG2 = 1$ " is satisfied in the P state shown in FIGS. 14 and 15, a next state to transit to is the P state ( $B7 - P$  in FIG. 22). When returning to this state, CHG2 is reset and CHG1 is set, and hence a next state to transit to is an I state ( $B8 \rightarrow I2$  in FIG. 22). When entering this state, CHG1, M, N are  
15           initialized, and the scene change detecting flag ChgSet on the reading side is set to 1.

          A next state to transit to is unconditionally the B state, after which ordinary transitions are performed. On reading side, since operation starts with the number of  
20           repeated B pictures being 2, the state transits from  $I1 \rightarrow B1 \rightarrow B2 \rightarrow P1 \rightarrow B3 \rightarrow B4 \rightarrow P2 \rightarrow B5 \rightarrow B6 \rightarrow B7 - P$ .

          In the P state shown in FIGS. 16 and 17, since the condition of " $ChgSet = 1, MC = 0$ " is satisfied, a next state to transit to is a C CHG state (a reading sequence  
25            $I2$  shown in FIG. 22). At this time, N is initialized, and the state is unconditionally transited to the P state (the reading sequence P3 shown in FIG. 22) with next timing.

When transiting to the P state, N is decremented, M is initialized, and ChgSet is reset, after which ordinary operation is performed. Therefore, as shown in FIG. 22, it is possible to set a scene changing view to an I picture and inhibit a reference to a predicted value across and over the scene changing view.

According to the present invention, as described above, since it is possible to automatically converge to an optimum value representing the number of repeated B pictures from the magnitude of the motion vector, the MC predictive error, and the interframe difference, the coding efficiency and the image quality can be improved. When a scene change occurs, an I picture is inserted. Therefore, any increase in the amount of information caused by a scene change can be made smaller than with the conventional process the period of I pictures is fixed, so that the coding efficiency and the image quality can be improved.

As described above, the image control apparatus according to the present invention compares the motion vector with a threshold, the motion-compensated predictive error with a threshold, and the difference produced by subtracting the motion-compensated predictive error from the interframe difference with a threshold, and adaptively controls the number of repeated B pictures based on the compared results. Therefore, it is possible to achieve a coding control process suitable for the pattern and motion



of an input image, for thereby improving the coding efficiency and the image quality.

Furthermore, the image control apparatus detects the occurrence of a scene change based on the interframe  
5 difference average, and inserts an I picture into a stream when a scene change is detected. Since the image control apparatus can perform a coding control process suitable for the pattern and motion of an input image, the coding efficiency and the image quality can be improved.

10 The foregoing is considered as illustrative only of the principles of the present invention. Further, since numerous modifications and changes will readily occur to those skilled in the art, it is not desired to limit the invention to the exact construction and applications shown  
15 and described, and accordingly, all suitable modifications and equivalents may be regarded as falling within the scope of the invention in the appended claims and their equivalents.